International Journal of Recent Development in Engineering and Technology
Website: www.ijrdet.com (ISSN 2347 - 6435 (Online) Volume 14, Issue 1, January 2025)

Review of Three-Phase Switched-Capacitor Multilevel Inverter Fed Induction Motor Drive

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***Abstract*— Multilevel inverters (MLIs) have gained significant attention in recent years due to their ability to generate high-quality output voltage with reduced harmonic distortion and lower switching losses. Among various multilevel inverter topologies, the three-phase switched-capacitor multilevel inverter (SCMLI) stands out due to its ability to boost voltage levels without the need for complex transformer-based circuits or bulky DC-link capacitors. This paper presents a comprehensive review of the three-phase switched-capacitor multilevel inverter (SCMLI) topology applied to induction motor (IM) drives. The SCMLI architecture enhances the efficiency and performance of induction motors by providing a stepped voltage waveform with minimal harmonic distortion, improving power quality and torque characteristics.**

Keywords—Multilevel Inverter, Fed Induction, Motor Drive, Three-Phase, Switched-Capacitor.

I. INTRODUCTION

Induction motor (IM) drives play a crucial role in modern industrial applications, electric vehicles, and renewable energy systems due to their robustness, high efficiency, and minimal maintenance requirements. However, the performance of induction motors is significantly influenced by the quality of the power supply, making the choice of inverter technology a critical factor. Traditional two-level inverters, while widely used, suffer from drawbacks such as high switching losses, increased harmonic distortion, and significant voltage stress on power semiconductor devices. These limitations have driven research toward multilevel inverters (MLIs), which provide superior voltage waveforms, reduced harmonic content, and improved motor performance.

Among the various multilevel inverter topologies, the switched-capacitor multilevel inverter (SCMLI) has emerged as a promising alternative due to its inherent voltage-boosting capability, reduced component count, and self-voltage balancing properties. Unlike conventional MLIs that rely on multiple isolated DC sources or bulky transformers, SCMLIs utilize capacitor switching techniques to generate stepped voltage levels, thereby reducing the need for additional power supply units. This feature makes SCMLIs particularly attractive for applications requiring compact and efficient power conversion solutions.

A three-phase SCMLI is specifically designed to drive induction motors with enhanced power quality and reduced harmonic distortion. By generating multiple voltage levels, SCMLIs ensure a near-sinusoidal output waveform, minimizing torque ripples and improving the overall efficiency of motor drives. Additionally, the voltage-boosting capability of SCMLIs eliminates the need for complex transformer-based circuits, making them a cost-effective and energy-efficient choice for high-power motor drive applications. These advantages make SCMLIs ideal for use in electric vehicle propulsion systems, renewable energy integration, and industrial automation.

One of the key advantages of SCMLIs is their self-voltage balancing feature, which eliminates the requirement for active voltage control circuits. In conventional MLIs, capacitor voltage fluctuations can lead to unbalanced voltage levels, reducing system stability and efficiency. However, SCMLIs inherently manage voltage balancing, ensuring consistent performance across all operating conditions. This self-balancing property not only simplifies circuit design but also enhances reliability and reduces maintenance requirements, making SCMLIs highly suitable for long-term industrial applications.

Despite these advantages, SCMLIs present several challenges that require further investigation. One of the primary concerns is the issue of capacitor voltage fluctuations during high-speed operation, which can affect the stability of the inverter output. Additionally, the increased number of switching devices in SCMLIs, although lower than in conventional MLIs, still introduces switching losses and control complexities. Researchers are actively exploring novel control strategies, including advanced pulse-width modulation (PWM) techniques and artificial intelligence (AI)-based optimization methods, to enhance the performance and efficiency of SCMLI-based induction motor drives.

The integration of SCMLIs with advanced control strategies is an area of active research aimed at improving system efficiency and dynamic response. Various modulation techniques, such as selective harmonic elimination (SHE), space vector pulse-width modulation (SVPWM), and model predictive control (MPC), have been proposed to optimize the switching operation of SCMLIs. Additionally, AI-based techniques, including machine learning and deep learning algorithms, are being explored for real-time voltage control and fault detection in SCMLI-based motor drives. These advancements are expected to further enhance the adaptability and reliability of SCMLIs in various applications.

II. LITERATURE SURVEY

S. K. Dalai et al.,[1] investigates the speed control of a three-phase switched-capacitor multilevel inverter (TSCMLI)-fed induction motor (IM) drive in order to enhance its performance. A multilevel inverter generates stepped sinusoidal waveforms of lower harmonics with increasing levels. The TSCMLI of 7- level line voltages synthesized using phase-disposition pulse width modulation (PD-PWM). The multi-level inverter output with the least THD is fed to the induction motor, and its speed is regulated by controlling the slip speed and maintaining constant V/f. A PI controller is implemented to ensure that the motor speed remains at its reference level.

P. R. Bhimireddy et al.,[2] a staggered inverter (MLI) plot is present for four-post (4-shaft), nine-phase (9-phase) induction motor (IM) drives with improved dc transport usage just as decreased gadget check. The present MLI is acknowledged with nine three-switch inverter legs (3-SIL) and a solitary dc source. This lower M.I will bring about a necessity of the greater greatness of dc-connect voltage to accomplish the appraised load voltage prerequisite. In this article, a successful phase reconfiguring idea is present for diminishing the dc-connect voltage necessity of the present MLI. What's more, every one of the prospects of phase reconfiguring subtleties for present MLI-fed 9-phase IM drive are likewise introduced.

B. P. Reddy et al.,[3] presented a three-level inverter geography for nine-phase shaft phase-balanced induction motor (PPMIM) drives by utilizing the two indistinguishable voltage profile loops (IVPC) of a four-post stator winding. When all is said in done, the two IVPCs of each phase winding are associated in arrangement, however in this article, these two IVPCs are isolated and energized with customary two-switch inverter legs without upsetting the motion per shaft and other machine boundaries. The 18 IVPCs of four-shaft nine-phase induction motor (IM) are energized with 18 two-switch inverter legs and fed by a similar dc source. Every two-switch inverter leg creates the two-level voltage across each IVPC winding, i.e., the resultant voltage seen by the phase winding is a three-level voltage waveform.

S. Shi et al.,[4] The converter comprising of a solitary phase half-connect rectifier and four-switch three-phase inverter is a low-cost power converter with muddled working requirements. It is hard to control by the regular techniques. This investigation proposes a half breed prescient control methodology with double circles for this converter. In the external circle, a relative fundamental regulator is intended to direct the dc-connect voltage, capacitor voltage adjusting, and the speed and motion of induction motors (IM). Likewise, in the inward circle, the limited set model prescient control is utilized to control the air conditioner input current and stator currents of the IM.

Z. M. Elbarbary et al.,[5] present a fuzzy logic regulator (FLC) to moderate the adverse consequences of motor boundary variety impact on backhanded rotor field arranged

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control (IRFOC) without proposing a tuning strategy. In any case, neither rotor nor polarizing inductances were examined. All things being equal, the variety in stator obstruction and burden inactivity which have less impact on the direction calculation is illustrated. This correspondence presents a typical framework model with similar simulation stage and boundaries. With indistinguishable tests, two blunders to the first work have been found.

M. S. Zaky et al.,[6] presents a speed regulator utilizing a fuzzy-logic regulator (FLC) for aberrant field-arranged control (IFOC) of induction motor (IM) drives fed by a four-switch three-phase (FSTP) inverter. In the present approach, the IM drive framework is fed by a FSTP inverter rather than the conventional six-switch three-phase (SSTP) inverter for practical low-power applications. The present FLC improves dynamic reactions, and it is likewise planned with decreased calculation trouble. The total IFOC plot joining the FLC for IM drives fed by the present FSTP inverter is underlying MATLAB/Simulink, and it is likewise experimentally implemented continuously utilizing a DSP-DS1103 control board for a model 1.1-kW IM.

I. N. El Badsi et al.,[7] Pulse width modulation (PWM) variable speed drives are progressively applied in numerous modern applications that require predominant execution. The transporter based PWM procedure can be improved by adding distinctive zero succession signals to the reference sinusoidal phase voltages. This work presents a similar assessment of five transporter based PWM procedures with zero-arrangement signal infusion for a six-switch three-phase inverter (B6) fed an open-circle induction motor (IM) drives over the direct modulation and overmodulation ranges. The transporter based PWM procedures viable are tried and are checked by experiments utilizing a test seat.

D. Zhou et al.,[8] Four-switch three-phase inverter-fed induction motor drive is alluring in light of the fact that it tends to be used in deficiency to-lerant control to settle the open/hamper of the six-switch three-phase inverter without excess power switches. Nonetheless, the reasonable three-phase current falls because of vacillation of the dc-interface capacitor. Allowing imperatives included, the prescient torque control (PTC) can be used for superior close-circle control of four-switch three-phase inverter-fed induction motor drive.

C. Ashfak et al.,[9] presents direct torque control (DTC) conspire for an induction motor (IM) fed from four switch three phase inverter (FSTPI) utilizing simple organized fake neural organization for switching vector determination and is considered to be a financially feasible arrangement having low expense, high proficiency and vigor. By contrasting the present plot and traditional DTC for IM fed from six switch and four switch three phase inverter shows fascinating execution. Space vector pulse width modulation (SVPWM) is the best method in correlation with pulse width modulation (PWM) or sinusoidal pulse width modulation (SPWM) in view of its lesser absolute consonant mutilation, more extensive straight modulation range, their simpler computerized acknowledgment and better DC transport usage for getting the switching voltage vectors. Simulation results show the legitimacy of the present plot.

D. Zhou et al.,[10] The four-switch three-phase (B4) inverter, having a lower number of switches, was first introduced for the chance of lessening the inverter cost, and it turned out to be exceptionally alluring as it tends to be used in issue open minded control to tackle the open/impede of the six-switch three-phase (B6) inverter. Be that as it may, the equilibrium among the phase currents implodes because of the variance of the two dc-connect capacitor voltages; hence, its application is limited. This work proposes a prescient torque control (PTC) plot for the B4 inverter-fed induction motor (IM) with the dc-connect voltage balance concealment. The voltage vectors of the B4 inverter under the vacillation of the two dc-connect capacitor voltages are inferred for exact expectation and control of the torque and stator motion. The three-phase currents are compelled to remain balance by straightforwardly controlling the stator motion.

III. CHALLENGES

Despite the numerous advantages of three-phase switched-capacitor multilevel inverters (SCMLIs) in induction motor drives, several challenges must be addressed to optimize their performance and ensure widespread adoption in industrial applications. These challenges primarily stem from issues related to voltage balancing, efficiency, control complexity, and component limitations. Addressing these concerns is crucial to improving the reliability, efficiency, and practicality of SCMLIs in real-world applications.

1. Capacitor Voltage Balancing Issues

One of the most significant challenges in SCMLIs is the uneven voltage distribution across capacitors, which can lead to instability and reduced performance. Although SCMLIs possess inherent voltage-balancing properties, rapid load variations and dynamic operating conditions may cause voltage imbalance between capacitors. This imbalance can lead to overcharging or undercharging of capacitors, affecting the quality of the inverter output waveform and ultimately reducing the efficiency of the induction motor drive. Advanced control strategies and real-time monitoring mechanisms are required to mitigate this issue.

2. High Switching Losses and Increased Switching Stress

SCMLIs involve multiple semiconductor switches that operate at high frequencies to generate stepped voltage waveforms. This results in higher switching losses compared to conventional two-level inverters. Additionally, high voltage stress on switching devices can reduce their lifespan and lead to potential failures. The increased number of switching operations also generates electromagnetic interference (EMI), which can adversely affect the performance of nearby electronic devices. Efficient switching strategies, such as optimized pulse-width modulation (PWM) techniques, are necessary to minimize switching losses and stress.

3. Increased Control Complexity

Compared to traditional inverter topologies, SCMLIs require more complex control mechanisms to regulate capacitor voltages, optimize switching sequences, and ensure stable operation under dynamic conditions. The control complexity increases further when integrating advanced modulation techniques such as space vector pulse-width modulation (SVPWM) or predictive control methods. Implementing these sophisticated control algorithms demands high-speed microcontrollers or digital signal processors (DSPs), increasing system cost and computational burden.

4. Limited Fault Tolerance and Reliability Concerns

SCMLIs, particularly those relying on capacitor-based voltage boosting, are more susceptible to capacitor failures compared to other MLI topologies. Capacitors degrade over time due to temperature variations, excessive charging cycles, and leakage currents, potentially leading to system failures or reduced efficiency. Additionally, failure of a switching device in SCMLIs can significantly impact overall performance, requiring redundancy mechanisms and fault detection techniques to improve system reliability.

5. High Component Count and Increased Circuit Complexity

Although SCMLIs reduce the number of required DC sources, they still require multiple capacitors and semiconductor switches, increasing the overall component count. This raises costs, space requirements, and circuit complexity, making practical implementation more challenging. Additionally, the size and weight of capacitors used for energy storage and voltage boosting can be a limiting factor in compact applications such as electric vehicles (EVs) and portable industrial drives.

6. Harmonic Distortion and Output Voltage Quality

While SCMLIs generate stepped voltage waveforms that approximate sinusoidal output, residual harmonic distortion may still be present, affecting the efficiency of induction motor drives. Total harmonic distortion (THD) must be minimized to prevent excessive motor heating, torque ripples, and power losses. The challenge lies in designing optimal switching sequences and modulation techniques that further reduce harmonic content without increasing switching losses.

IV. CONCLUSION

Three-phase switched-capacitor multilevel inverters (SCMLIs) offer significant advantages in induction motor drives, including improved voltage waveforms, reduced harmonic distortion, and enhanced efficiency. However, challenges such as capacitor voltage balancing, high switching losses, control complexity, and thermal management must be addressed to optimize their performance. Advancements in modulation techniques, fault-tolerant designs, and AI-based control strategies can help overcome these limitations, making SCMLIs more reliable and efficient for industrial and renewable energy applications. Continued research and development in power electronics and control methodologies will be crucial for the widespread adoption of SCMLIs in next-generation motor drive systems.

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